

THE COMMISSIONER FOR PATENTS
P.O.Box 1450, Alexandria, VA 22313-1450

Docket No. TS01-999

Sir:

Transmitted herewith for filing is the Patent Application of:

Inventor: **SHI-CHI LIN**

For: **METHOD OF MANUFACTURING DIELECTRIC ISOLATED SILICON STRUCTURE**

16424 U.S. PTO
10/650601
08/28/03


Enclosed are:

3 sheets of drawing(s) - formal.

An assignment of the invention to **Taiwan Semiconductor Manufacturing Co.**

An associate power of attorney Applicant claims small entity status

Request & Certification under 35 USC 122(b)(2)(b)(i)

The filing fee has been calculated as shown below:

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Stephen B. ACKERMAN, REG. NO. 37,761

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